

ABSTRACT

A method for determining the amount of deterministic jitter and random jitter in a digital signal having transitions between logical levels, the method comprising the steps of: providing said digital signal, determining a plurality of bit error rate values, each bit error rate value being associated with one of a plurality of successive timing points, and each bit error rate value being derived from a comparison of a result of a detection of a transition occurring in the digital signal cumulatively prior to its associated timing point with an expected signal, applying a polynomial fit in time to said plurality of bit error rate values associated with said timing points for determining a number of polynomial coefficients of said polynomial fit, and deriving the amount each of said deterministic and said random jitter from said polynomial coefficients.